

(FILE 'HOME' ENTERED AT 10:33:53 ON 07 MAR 2000)

FILE 'USPATFULL' ENTERED AT 10:34:00 ON 07 MAR 2000

L1 1395514 S CONNECTION OR PATH OR CHANNEL
L2 3688 S L1(W)IDENTIF?
L3 51339 S INITIATOR OR REQUESTER
L4 143268 S TARGET OR RESPONDER
L5 1 S L2(P)L3(P)L4
L6 727 S (THREAD OR TRANSACTION) (A)IDENTIF?
L7 4179 S L1(A)IDENTIF?
L8 1 S L3(P)L4(P)L7
L9 13392 S L1(3A)IDENTIF?
L10 2 S L3(P)L4(P)L9
L11 150108 S (DATA OR INFORMATION) (2A) (TRANSFER? OR TRANSMI?)
L12 24 S L6(7A)L11
L13 14 S L12 AND L9
L14 7 S L13 AND L3 AND L4

L4 ANSWER 6 OF 6 USPATFULL
 AN 94:31792 USPATFULL
 TI Protocol processing apparatus for use in interfacing
 network connected
 computer systems utilizing separate paths for control
 information and
 data transfer
 IN Yokoyama, Tatsuya, Machida, Japan
 Matsui, Susumu, Machida, Japan
 Terada, Matsuaki, Machida, Japan
 Hirata, Tetsuhiko, Machida, Japan
 Mizutani, Mika, Tokyo, Japan
 PA Hitachi, Ltd., Tokyo, Japan (non-U.S. corporation)
 PI US 5303344 19940412
 AI US 1991-659705 19910225 (7)
 RLI Continuation-in-part of Ser. No. US 1990-489243, filed on 5
 Mar 1990,
 now patented, Pat. No. US 5056058, issued on 8 Oct 1991
 PRAI JP 1990-41005 19900223
 JP 1990-311494 19901119
 DT Utility
 EXNAM Primary Examiner: Kulik, Paul V.; Assistant Examiner:
 Amsbury, Wayne
 LREP Antonelli, Terry, Stout & Kraus
 CLMN Number of Claims: 22
 ECL Exemplary Claim: 1
 DRWN 63 Drawing Figure(s); 57 Drawing Page(s)
 LN.CNT 1811
 DETD The main processor 70 within the computer 1, when supplying
 a command
 of, for example, a data **transmission request** to the
 protocol processor 10, previously writes transmission data
 in the
 transmission buffer region of the buffer memory 30, sets
 parameters of a
 command code, **connection identifying** element,
 transmission data length and transmission buffer address in
 the fields
 F1, to F4 of the first entry E1 of the transmitting command
 descriptor
 31 defined in the buffer memory 30, and then supplies the
 address
 information (hereinafter, referred to as CCID) indicating
 the head of
 the command descriptor 31 through the primitive path 22-1
 to the
 protocol processor 10. The protocol processor 10 is
 actuated by the
 CDID. In other words, in this invention, the interface
 between layers is
 controlled by the transmission and reception of the CDID
 through the

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Most Frequently Occurring Classifications of Patents Returned
From A Search of 09191291 on March 07, 2000

Original Classifications

5 710/74
3 700/95
3 709/222
2 379/10

Cross-Reference Classifications

13 364/DIG 1
7 711/112
6 364/230
6 364/DIG 2
5 364/221
5 364/221.9
5 364/230.3
5 364/230.4
5 364/242.94
5 364/248.1
5 710/105
5 710/129
5 710/5
4 364/221.7
4 364/237.8
4 364/238.3
4 364/242.1
4 364/259
4 364/262.4
4 364/262.5
4 364/281.3
4 364/281.8
4 379/15
4 379/34
3 340/825.34
3 364/221.2
3 364/222
3 364/222.81
3 364/228
3 364/230.1
3 364/234
3 364/236.2
3 364/282.1
3 379/29
3 709/223
2 340/825.3
2 360/97.01

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2 364/221.4
2 364/222.2
2 364/222.82
2 364/228.3
2 364/229
2 364/235
2 364/237.2
2 364/240
2 364/241.2
2 364/242.95
2 364/242.96
2 364/245
2 364/245.5
2 364/248
2 364/248.3
2 364/259.3
2 364/259.5
2 364/260
2 364/260.1
2 364/260.4
2 364/260.6
2 364/264
2 364/280
2 364/281.7
2 364/282.3
2 364/283.1
2 364/DIG.1
2 379/10
2 379/14
2 455/410
2 700/2
2 709/243
2 710/266

Combined Classifications

13 364/DIG 1
7 711/112
6 364/230
6 364/DIG 2
6 710/129
6 710/5
5 364/221
5 364/221.9
5 364/230.3
5 364/230.4
5 364/242.94
5 364/248.1
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5 710/74
4 364/221.7
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2 710/266
2 710/52
2 710/63

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5216613
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